Errata of Publications

Publication II Each encryption has a latency of 43 clock cycles and Table 2 should read as follows:

	Virtex-II XC2V2000-5	Virtex-E XCV1000E-8
Throughput (Gbps)	17.8	16.5
Clock frequaency (MHz)	139.1	129.2
Clock cycle (ns)	7.19	7.74
Latency (ns)	309.17	332.82
Slices	10750	11719

Publication VI On page 7, Sec. 4.1, above (18), the equation computed by the multiplier should be $c(x) = a(x)b(x) \mod p(x)$.

Publication IX The last sentence of Sec. 4.2 should read as follows: "As a downside, the calculation of an integer equivalent has a longer computation time."